

# **Rajeev Thakur**

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## **Education**

- Ph.D., Computer Engineering, Syracuse University, Syracuse, NY, 1995
- M.S., Computer Engineering, Syracuse University, Syracuse, NY, 1992
- B.E., Computer Engineering, University of Mumbai, India, 1990

## **Work Experience and Positions Held**

- Mathematics and Computer Science Division, Argonne National Laboratory, Argonne, IL
  - Deputy Director, June 2012–present
  - Senior Computer Scientist, November 2011–present
  - Computer Scientist, April 2002–October 2011
  - Assistant Computer Scientist, May 1997–April 2002 (on leave December 2000–December 2001)
  - Postdoctoral Researcher, May 1995–April 1997
- Computation Institute, University of Chicago, Chicago, IL
  - Senior Fellow, April 2010–present
  - Fellow, August 2006–March 2010
- Dept. of Electrical Engineering and Computer Science, Northwestern University, Evanston, IL
  - Adjunct Professor, September 2009–August 2015
  - Adjunct Associate Professor, September 2006–August 2009
- Prismedia Networks (Acirro), Inc., San Jose, CA
  - Senior Scientist, December 2000–December 2001

## **Research Interests**

High-performance computing, parallel programming models, runtime systems, communication libraries, scalable parallel I/O

## **Honors and Awards**

- R&D 100 Award for “MPICH2: High Performance, Portable Implementation of the Message Passing Interface Standard for Parallel Computing,” 2005
- Selected to participate in Argonne’s Strategic Laboratory Leadership Program taught by the University of Chicago Booth School of Business, 2011
- Two outstanding papers at Euro PVM/MPI 2009
- Best Paper Award at the International Supercomputing Conference (ISC) 2009
- Finalist for Best Paper Award and Best Student Paper Award at SC08
- One outstanding paper at Euro PVM/MPI 2008

- Best Paper Award at the International Supercomputing Conference (ISC) 2008
- Two outstanding papers at Euro PVM/MPI 2007
- Two outstanding papers at Euro PVM/MPI 2006
- Best Poster Award, SC2003: High Performance Networking and Computing
- Best Paper Award, 4th Annual Linux Showcase and Conference, October 2000
- Best Student Paper Award at Supercomputing '92 in the category of performance measurement, November 1992

## Major Software Developed

1. **MPICH** I have been involved in the development of the MPICH implementation of MPI for many years. MPICH is used by thousands of users all over the world and forms the basis of vendor MPI implementations from IBM, Cray, Intel, Microsoft, and other vendors. The majority of the largest supercomputers in the world run MPICH-based MPI implementations.
2. **ROMIO** I am the original developer of the ROMIO implementation of MPI-IO, which is used as the MPI-IO implementation in MPICH and other MPI implementations.

## International Standards Organizations

- I have been an active member of the MPI Forum since the mid-1990s. I participated in the definition of MPI-2 in 1995-97, particularly the interface for parallel I/O in MPI-2 (MPI-IO).
- More recently, I have participated in the MPI Forum for the definition of MPI 2.1, MPI 2.2, and MPI-3 standards. Together with Bill Gropp and Torsten Hoefler, I am co-editor of the One-Sided Communication chapter in MPI-3.

## Professional Activities

- Steering Committee, SC Conference Series, January 2015–present
- Treasurer, ACM SIGHPC, July 2016–present
- Member-at-Large, ACM SIGHPC, July 2013–June 2016
- Steering Committee, International Workshop on Data-Intensive Scalable Computing Systems (DISCS), 2012–present
- Editorial Advisory Board, Scientific Programming, November 2012–November 2014
- Associate Editor, IEEE Transactions on Parallel and Distributed Systems, 2004–2007
- Program Co-Chair, 24th European MPI Users Group Conference (EuroMPI/USA), Argonne, Illinois, September 2017
- Program Committee, IEEE/ACM 17th International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2017), Madrid, Spain, May 2017
- Awards Co-Chair, SC16: International Conference on High Performance Computing, Networking, Storage, and Analysis, Salt Lake City, Utah, November 2016
- Program Committee, Second International Workshop on Extreme Scale Programming Models and Middleware (ESPM2 2016) at SC16, Salt Lake City, Utah, November 2016
- Tutorials Committee, International Supercomputing Conference (ISC'16), Frankfurt, Germany, June 2016
- Program Committee, International Conference on Supercomputing (ICS 2016), Istanbul, Turkey, June 2016

- Program Committee, 30th IEEE International Parallel & Distributed Processing Symposium (IPDPS 2016), Chicago, Illinois, May 2016
- Program Committee, IEEE/ACM 16th International Symposium on Cluster, Cloud and Grid Computing (CCGrid 2016), Cartagena, Columbia, May 2016
- Program Committee, First International Workshop on Extreme Scale Programming Models and Middleware (ESPM2 2015) at SC15, Austin, Texas, November 2015
- Program Committee, 24th International Conference on Parallel Architectures and Compilation Techniques (PACT 2015), San Francisco, California, October 2015
- Program Committee, 22nd European MPI Users Group Conference (EuroMPI), Bordeaux, France, September 2015
- Program Committee, 44th International Conference on Parallel Processing (ICPP 2015), Beijing, China, September 2015
- Program Committee, 21st International European Conference on Parallel and Distributed Computing (Euro-Par 2015), Vienna, Austria, August 2015
- Program Committee, 29th IEEE International Parallel & Distributed Processing Symposium (IPDPS 2015), Hyderabad, India, May 2015
- Tutorials Committee, SC14: International Conference on High Performance Computing, Networking, Storage, and Analysis, New Orleans, Louisiana, November 2014
- Program Committee, 21st European MPI Users Group Conference (EuroMPI/Asia), Kyoto, Japan, September 2014
- Program Committee, 28th International Conference on Supercomputing (ICS 2014), Munich, Germany, June 2014
- Program Committee, 28th IEEE International Parallel & Distributed Processing Symposium (IPDPS 2014), Phoenix, Arizona, May 2014
- Program Committee, Sixth International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2013, Lyon, France, October 2013
- Program Committee, 20th European MPI Users Group Conference (EuroMPI), Madrid, Spain, September 2013
- Program Committee, 27th International Conference on Supercomputing (ICS 2013), Eugene, Oregon, June 2013
- Technical Program Chair, SC12: International Conference on High Performance Computing, Networking, Storage, and Analysis, Salt Lake City, Utah, November 2012
- Program Committee, 19th European MPI Users Group Conference (EuroMPI), Vienna, Austria, September 2012
- Program Committee, 41st International Conference on Parallel Processing (ICPP-11), Pittsburgh, Pennsylvania, September 2012
- Program Committee, Fifth International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2012, Pittsburgh, Pennsylvania, September 2012
- Program Committee, 10th IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA 2012), Madrid, Spain, July 2012
- Tutorials Committee, International Supercomputing Conference (ISC'12), Hamburg, Germany, June 2012
- Program Committee, IEEE International Symposium on Cluster Computing and the Grid (CC-Grid 2012), Ottawa, Canada, May 2012
- Technical Papers Co-Chair, SC11: International Conference on High Performance Computing, Networking, Storage, and Analysis, Seattle, Washington, November 2011

- Program Committee, 2011 IEEE International Symposium on Workload Characterization, Austin, Texas, November 2011
- Program Committee, IEEE International Conference on Cluster Computing, Austin, Texas, September 2011
- Program Committee, 18th European MPI Users Group Conference (EuroMPI), Santorini, Greece, September 2011
- Program Committee, 40th International Conference on Parallel Processing (ICPP-11), Taipei, Taiwan, September 2011
- Program Committee, Fourth International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2011, Taipei, Taiwan, September 2011
- Program Committee, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging (PADTAD 2011), Toronto, Canada, July 2011
- Program Committee, 25th International Conference on Supercomputing (ICS 2011), Tucson, Arizona, June 2011
- Tutorials Committee, International Supercomputing Conference (ISC'11), Hamburg, Germany, June 2011
- Program Committee, IEEE International Symposium on Cluster Computing and the Grid (CC-Grid 2011), Newport Beach, California, May 2011
- Program Committee, 7th IEEE International Workshop on Storage Network Architecture and Parallel I/O (SNAPI 2011), Denver, Colorado, May 2011
- Program Vice-Chair for communication networks, 17th IEEE International Conference on High Performance Computing (HiPC '10), Goa, India, December 2010
- System Software Area Co-Chair for Technical Papers, SC10: International Conference on High Performance Computing, Networking, Storage, and Analysis, New Orleans, Louisiana, November 2010
- Program Committee, 39th International Conference on Parallel Processing (ICPP-10), San Diego, California, September 2010
- Program Committee, Third International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2010, San Diego, California, September 2010
- Program Committee, 17th European MPI Users Group Conference (EuroMPI), Stuttgart, Germany, September 2010
- Program Committee, 5th IEEE International Conference on Networking, Architecture, and Storage (NAS 2010), Macau, Macau SAR, China, July 2010
- Program Committee, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging (PADTAD 2010), Trento, Italy, July 2010
- Program Committee, 24th International Conference on Supercomputing (ICS 2010), Tsukuba, Japan, June 2010
- Program Committee, 6th IEEE International Workshop on Storage Network Architecture and Parallel I/O (SNAPI 2010), Incline Village, Nevada, May 2010
- Co-Chair, Student Research Symposium, 16th IEEE International Conference on High Performance Computing (HiPC '09), Cochin, India, December 2009
- Tutorials Co-Chair, SC09: International Conference on High Performance Computing, Networking, Storage, and Analysis, Portland, Oregon, November 2009
- Technical Papers Committee, SC09: International Conference on High Performance Computing, Networking, Storage, and Analysis, Portland, Oregon, November 2009

- Program Committee, Third Conference on Partitioned Global Address Space Programming Models (PGAS 2009), Ashburn, Virginia, October 2009
- Program Committee, IEEE International Conference on Cluster Computing, New Orleans, Louisiana, September 2009
- Program Committee, 16th European PVM/MPI Users Group Conference (Euro PVM/MPI), Helsinki, Finland, September 2009
- Program Committee, Second International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2009, Vienna, Austria, September 2009
- Program Committee, 38th International Conference on Parallel Processing (ICPP-09), Vienna, Austria, September 2009
- Program Committee, Workshop on Parallel and Distributed Systems: Testing, Analysis, and Debugging (PADTAD 2009), Chicago, Illinois, July 2009
- Co-Chair, Student Research Symposium, 15th IEEE International Conference on High Performance Computing (HiPC '08), Bangalore, India, December 2008
- Program Committee, 14th IEEE International Conference on Parallel and Distributed Systems (ICPADS'08), Melbourne, Australia, December 2008
- Technical Papers Committee, SC08: International Conference on High Performance Computing, Networking, Storage, and Analysis, Austin, Texas, November 2008
- Program Committee, First International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2) at ICPP 2008, Portland, Oregon, September 2008
- Program Committee, 37th International Conference on Parallel Processing (ICPP-08), Portland, Oregon, September 2008
- Program Committee, Thirteenth Asia-Pacific Computer Systems Architecture Conference, Taiwan, August 2008
- Poster/Presentation Chair, 14th IEEE International Conference on High Performance Computing (HiPC '07), Goa, India, December 2007
- Program Committee, IEEE International Conference on Cluster Computing, Austin, Texas, September 2007
- Program Committee, 2007 IEEE International Symposium on Workload Characterization, Boston, Massachusetts, September 2007
- Program Committee, Twelfth Asia-Pacific Computer Systems Architecture Conference, Seoul, Korea, August 2007
- Program Committee, Third Workshop on System Management Techniques, Services, and Processes at IPDPS 2007, Long Beach, California, March 2007
- Poster/Presentation Chair, 13th IEEE International Conference on High Performance Computing (HiPC '06), Bangalore, India, December 2006
- Program Committee, Twelfth International Conference on Parallel and Distributed Systems, Minneapolis, Minnesota, July 2006
- Program Committee, Eleventh Asia-Pacific Computer Systems Architecture Conference, Shanghai, China, June 2006
- Program Committee, Second Workshop on System Management Tools for Large-Scale Parallel Systems at IPDPS 2006, Rhodes Island, Greece, April 2006
- Poster/Presentation Chair, 12th IEEE International Conference on High Performance Computing (HiPC '05), Goa, India, December 2005

- Program Committee, Tenth Asia-Pacific Computer Systems Architecture Conference, Singapore, October 2005
- Program Committee, First Workshop on System Management Tools for Large-Scale Parallel Systems at IPDPS 2005, Denver, Colorado, April 2005
- Program Committee, IEEE International Conference on Cluster Computing, San Diego, California, September 2004
- Program Committee, Ninth Asia-Pacific Computer Systems Architecture Conference, Beijing, China, September 2004
- Program Committee, International Conference on Parallel Processing, Montreal, Canada, August 2004
- Program Committee, IEEE International Conference on Cluster Computing, Hong Kong, December 2003
- Program Committee, Workshop on Parallel I/O in Cluster Computing and Computational Grids at CCGrid 2003, Tokyo, Japan, May 2003
- Vice Program Chair, Cluster 2002: IEEE International Conference on Cluster Computing, Chicago, Illinois, September 2002
- Editorial Board of Special Issue of *Calculateurs Parallèles* on Parallel I/O for Cluster Computing, 2001
- Program Committee, IEEE International Symposium on Cluster Computing and the Grid (CC-Grid'2001), Brisbane, Australia, May 2001
- Chair for Birds-of-a-Feather sessions, SC2000: High Performance Networking and Computing, Dallas, Texas, November 2000
- Global Chair, Parallel I/O and Storage Technology track at the European Conference on Parallel Computing (Euro-Par 2000), Munich, Germany, August 2000
- Program Committee, 6th International Conference on High Performance Computing (HiPC '99), Calcutta, India, December 1999
- General Co-Chair, Sixth Workshop on I/O in Parallel and Distributed Systems (IOPADS '99), Atlanta, Georgia, May 1999
- Program Committee, Heterogeneous Computing Workshop at IPPS '99, San Juan, Puerto Rico, April 1999
- Poster Exhibits Chair, SC98: High Performance Networking and Computing, Orlando, Florida, November 1998
- Vice-Chair, Parallel I/O Workshop at Euro-Par '98, Southampton, UK, September 1998
- Co-Guest Editor, Special Issue of the *International Journal of High Performance Computing Applications* on "I/O in Parallel Applications," 1998
- Tutorials Vice-Chair, SC97: High Performance Networking and Computing, San Jose, California, November 1997
- Program Committee, 4th International Conference on High Performance Computing (HiPC '97), Bangalore, India, December 1997
- Publicity Chair, Fifth Workshop on I/O in Parallel and Distributed Systems (IOPADS '97), San Jose, California, November 1997
- Tutorials Vice-Chair, Supercomputing '96, Pittsburgh, Pennsylvania, November 1996
  
- Reviewer for *IEEE Transactions on Parallel and Distributed Systems*, *Journal of Parallel and Distributed Computing*, *Parallel Computing*, *Concurrency and Computation: Practice and Experience*, *Parallel Processing Letters*, *The Journal of Supercomputing*, *The Computer Journal*,

*International Journal of High Performance Computing Applications*, and several conferences

- Served on Ph.D. thesis committees of Rakesh Krishnaiyer (1998), Chutimet Srinilta (1998), and Jaechun No (1999) at Syracuse University; Murali Vilayannur at Penn State University (2005); Sachin More (2000), Avery Ching (2007), Kenin Coloma (2007), and Arifa Nisar (2010) at Northwestern University; Yawei Li (2008) at Illinois Institute of Technology; Saba Sehrish (2010) at University of Central Florida

## Patent

1. “Method and Apparatus for Real-Time Parallel Delivery of Segments of a Large Payload File.” Inventors: Desmond Cho-Hung Chan, Siew Yong Sim, Rajeev Thakur, Trygve Isaacson, and William D. Gropp. US Patent 7076553, awarded July 11, 2006.

## Publications and Presentations

### Books

1. William Gropp, Torsten Hoefler, Rajeev Thakur, and Ewing Lusk, *Using Advanced MPI: Modern Features of the Message-Passing Interface*, MIT Press, November 2014.
2. William Gropp, Ewing Lusk, and Rajeev Thakur, *Using MPI-2: Advanced Features of the Message-Passing Interface*, MIT Press, November 1999. (Also translated into Japanese.)

### Refereed Journal Articles and Book Chapters

1. James Dinan, Pavan Balaji, Darius Buntinas, David Goodell, William Gropp, Rajeev Thakur, “An Implementation and Evaluation of the MPI 3.0 One-Sided Communication Interface,” *Concurrency and Computation: Practice and Experience*, 2016.
2. Ashwin M. Aji, Lokendra S. Panwar, Feng Ji, Karthik Murthy, Milind Chabbi, Pavan Balaji, Keith R. Bisset, James Dinan, Wu-chun Feng, John Mellor-Crummey, Xiaosong Ma, and Rajeev Thakur, “MPI-ACC: Accelerator-Aware MPI for Scientific Applications,” *IEEE Transactions on Parallel and Distributed Systems*, 27(5):1401–1414, May 2016.
3. William Gropp and Rajeev Thakur, “Message Passing Interface,” *Programming Models for Parallel Computing*, MIT Press, Ch. 1, pp. 1–21, 2015.
4. Torsten Hoefler, James Dinan, Rajeev Thakur, Brian Barrett, Pavan Balaji, and William Gropp, “Remote Memory Access Programming in MPI-3,” *ACM Transactions on Parallel Computing*, Vol. 7, No. 2, pp. 9:1–9:26, July 2015.
5. Yong Chen, Yin Lu, Prathamesh Amritkar, Rajeev Thakur, and Yu Zhuang, “Performance Model Directed Data Sieving for High Performance I/O,” *Journal of Supercomputing*, 71(6):2066–2090, June 2015.
6. Seong Jo Kim, Yuanrui Zhang, Seung Woo Son, Mahmut Kandemir, Wei-keng Liao, Rajeev Thakur, Alok Choudhary, “IOPro: A Parallel I/O Profiling and Visualization Framework for High-Performance Storage Systems,” *Journal of Supercomputing*, 71(3):840–870, March 2015.
7. Yin Lu, Yong Chen, Yu Zhuang, Jialin Liu, Rajeev Thakur, “Collective Input/Output under Memory Constraints,” *International Journal of High Performance Computing Applications*, 29(1):21–36, February 2015.

8. James Dinan, Ryan E. Grant, Pavan Balaji, David Goodell, Douglas Miller, Marc Snir, and Rajeev Thakur, “Enabling Communication Concurrency Through Flexible MPI Endpoints,” *International Journal of High Performance Computing Applications*, 28(4):390–405, November 2014.
9. Wei-keng Liao and Rajeev Thakur, “MPI-IO,” *High Performance Parallel I/O*, CRC Press, Ch. 13, pp. 157–169, October 2014.
10. John Jenkins, James Dinan, Pavan Balaji, Tom Peterka, Nagiza F. Samatova, and Rajeev Thakur, “Processing MPI Derived Datatypes on Noncontiguous GPU-resident Data,” *IEEE Transactions on Parallel and Distributed Systems*, 25(10):2627–2637, October 2014.
11. Anshu Dubey, Steve R. Brandt, Richard Brower, Merle Giles, Paul Hovland, Donald Q. Lamb, Frank Loffler, Boyana Norris, Brian W. O’Shea, Claudio Rebbi, Marc Snir, Rajeev Thakur, and Petros Tzeferacos, “Software Abstractions and Methodologies for HPC Simulation Codes on Future Architectures,” *Journal of Open Research Software*, 2(1):e14, pp. 1–5, 2014.
12. Torsten Hoefler, James Dinan, Darius Buntinas, Pavan Balaji, Brian Barrett, Ron Brightwell, William Gropp, Vivek Kale, and Rajeev Thakur, “MPI+MPI: A New, Hybrid Approach to Parallel Programming with MPI Plus Shared Memory Computing,” *Computing*, 95(12):1121–1136, December 2013.
13. Ganesh Gopalakrishnan, Robert M. Kirby, Stephen Siegel, Rajeev Thakur, William Gropp, Ewing Lusk, Bronis R. de Supinski, Martin Schulz, and Greg Bronevetsky, “Formal Analysis of MPI-Based Parallel Programs,” *Communications of the ACM*, 54(12):82–91, December 2011.
14. Pavan Balaji, Darius Buntinas, David Goodell, William Gropp, Torsten Hoefler, Sameer Kumar, Ewing Lusk, Rajeev Thakur, and Jesper Larsson Träff, “MPI on Millions of Cores,” *Parallel Processing Letters*, 21(1):45–60, March 2011.
15. Torsten Hoefler, Rolf Rabenseifner, Hubert Ritzdorf, Bronis R. de Supinski, Rajeev Thakur, and Jesper Larsson Träff, “The Scalable Process Topology Interface of MPI 2.2,” *Concurrency and Computation: Practice and Experience*, 23(4):293–310, March 2011.
16. P. Balaji, W. Feng, H. Lin, J. Archuleta, S. Matsuoka, A. Warren, J. Setubal, E. Lusk, R. Thakur, I. Foster, K. Shinpaugh, S. Coghlan, and D. Reed, “Global-scale Distributed I/O with ParaMEDIC,” *Concurrency and Computation: Practice and Experience*, 22(16):2266–2281, November 2010.
17. Zhiling Lan, Jiexing Gu, Ziming Zheng, Rajeev Thakur, and Susan Coghlan, “A Study of Dynamic Meta-Learning for Failure Prediction in Large-Scale Systems,” *Journal of Parallel and Distributed Computing*, 70(6):630–643, June 2010.
18. Jesper Larsson Träff, William D. Gropp, and Rajeev Thakur, “Self-Consistent MPI Performance Guidelines,” *IEEE Transactions on Parallel and Distributed Systems*, 21(5):698–709, May 2010.
19. Jesper Larsson Träff, Andreas Ripke, Christian Siebert, Pavan Balaji, and Rajeev Thakur, and William Gropp, “A Pipelined Algorithm for Large, Irregular Allgather Problems,” *International Journal of High Performance Computing Applications*, 24(1):58–68, Spring 2010.
20. Pavan Balaji, Darius Buntinas, David Goodell, William Gropp, and Rajeev Thakur, “Fine-Grained Multithreading Support for Hybrid Threaded MPI Programming,” *International Journal of High Performance Computing Applications*, 24(1):49–57, Spring 2010.
21. Pavan Balaji, Anthony Chan, William Gropp, Rajeev Thakur, and Ewing Lusk, “The Importance of Non-Data-Communication Overheads in MPI,” *International Journal of High Performance Computing Applications*, 24(1):5–15, Spring 2010.



22. Salman Pervez, Ganesh Gopalakrishnan, Robert M. Kirby, Rajeev Thakur, and William Gropp, "Formal Methods Applied to High Performance Computing Software Design: A Case Study of MPI One-Sided Communication Based Locking," *Software: Practice and Experience*, 40(1):23–43, January 2010.
23. Rajeev Thakur and William Gropp, "Test Suite for Evaluating Performance of Multithreaded MPI Communication," *Parallel Computing*, 35(12):608–617, December 2009.
24. Alok Choudhary, Wei-keng Liao, Kui Gao, Arifa Nisar, Robert Ross, Rajeev Thakur, and Robert Latham, "Scalable I/O and Analytics," *Journal of Physics: Conference Series (SciDAC 2009)*, Vol 180, 2009.
25. Pavan Balaji, Anthony Chan, Rajeev Thakur, William Gropp, and Ewing Lusk, "Toward Message Passing for a Million Processes: Characterizing MPI on a Massive Scale Blue Gene/P," *Computer Science – Research and Development*, 24(1-2):11–19, September 2009. (**Best Paper Award** at the International Supercomputing Conference (ISC) 2009)
26. P. Lai, P. Balaji, R. Thakur, and D. K. Panda, "ProOnE: A General Purpose Protocol Onload Engine for Multi- and Many-Core Architectures," *Computer Science – Research and Development*, 23(3-4):133–142, June 2009.
27. William Gropp and Rajeev Thakur, "Thread Safety in an MPI Implementation: Requirements and Analysis," *Parallel Computing*, 33(9):595–604, September 2007.
28. Robert Latham, Robert Ross, and Rajeev Thakur, "Implementing MPI-IO Atomic Mode and Shared File Pointers Using MPI One-Sided Communication," *International Journal of High Performance Computing Applications*, 21(2):132–143, Summer 2007.
29. Murali Vilayannur, Anand Sivasubramaniam, Mahmut Kandemir, Rajeev Thakur, and Robert Ross, "Discretionary Caching for I/O on Clusters," *Cluster Computing*, 9(1):29–44, January 2006.
30. Robert Ross, Rajeev Thakur, and Alok Choudhary, "Achievements and Challenges for I/O in Computational Science," *Journal of Physics: Conference Series (SciDAC 2005)*, (16):501–509, 2005.
31. Rajeev Thakur, William Gropp, and Brian Toonen, "Optimizing the Synchronization Operations in MPI One-Sided Communication," *International Journal of High Performance Computing Applications*, 19(2):119–128, Summer 2005.
32. Rajeev Thakur, Rolf Rabenseifner, and William Gropp, "Optimization of Collective Communication Operations in MPICH," *International Journal of High Performance Computing Applications*, 19(1):49–66, Spring 2005.
33. Rajeev Thakur, William Gropp, and Ewing Lusk, "ADIO: A Framework for High-Performance, Portable Parallel I/O," *Scalable Input/Output: Achieving System Balance*, MIT Press, Ch. 4, pp. 111–134, 2004.
34. Alok Choudhary, Mahmut Kandemir, Sachin More, Jaechun No, and Rajeev Thakur, "Collective I/O and Large-Scale Data Management," *Scalable Input/Output: Achieving System Balance*, MIT Press, Ch. 2, pp. 35–75, 2004.
35. Jaechun No, Rajeev Thakur, and Alok Choudhary, "High-Performance Scientific Data Management System," *Journal of Parallel and Distributed Computing*, 64(4):434–447, April 2003.
36. Rajeev Thakur and William Gropp, "Parallel I/O," *Sourcebook of Parallel Computing*, Morgan Kaufmann Publishers, Ch. 11, pp. 331–355, 2002.
37. Rajeev Thakur, William Gropp, and Ewing Lusk, "Optimizing Noncontiguous Accesses in MPI-IO," *Parallel Computing*, 28(1):83–105, January 2002.

38. Phillip Dickens and Rajeev Thakur, "Evaluation of Collective I/O Implementations on Parallel Architectures," *Journal of Parallel and Distributed Computing*, 61(8):1052–1076, August 1, 2001.
39. Dan Bonachea, Phillip Dickens, and Rajeev Thakur, "High-Performance File I/O in Java: Existing Approaches and Bulk I/O Extensions," *Concurrency and Computation: Practice and Experience*, 13(8-9):713–736, 2001.
40. A. Choudhary, M. Kandemir, J. No, G. Memik, X. Shen, W. Liao, H. Nagesh, S. More, V. Taylor, R. Thakur, and R. Stevens, "Data Management for Large-Scale Scientific Computations in High Performance Distributed Systems," *Cluster Computing*, 3(1):45–60, 2000.
41. Rajeev Thakur, Ewing Lusk, and William Gropp, "I/O in Parallel Applications: The Weakest Link," *International Journal of Supercomputer Applications and High Performance Computing*, 12(4):389–395, Winter 1998. (Guest Editors' Introduction)
42. Rajeev Thakur and Alok Choudhary, "An Extended Two-Phase Method for Accessing Sections of Out-of-Core Arrays," *Scientific Programming*, 5(4):301–317, Winter 1996.
43. Rajeev Thakur, Alok Choudhary, and J. Ramanujam, "Efficient Algorithms for Array Redistribution," *IEEE Transactions on Parallel and Distributed Systems*, 7(6):587–594, June 1996.
44. Rajeev Thakur, Alok Choudhary, Rajesh Bordawekar, Sachin More, and Sivaramakrishna Kuditipudi, "Passion: Optimized I/O for Parallel Applications," *IEEE Computer*, 29(6):70–78, June 1996.
45. Rajeev Thakur and Alok Choudhary, "Runtime Support for Out-of-Core Parallel Programs," *Input/Output in Parallel and Distributed Computer Systems*, Kluwer Academic Publishers, Ch. 6, pp. 147–165, 1996.
46. Rajeev Thakur, Ravi Ponnusamy, Alok Choudhary, and Geoffrey Fox, "Complete Exchange on the CM-5 and Touchstone Delta," *The Journal of Supercomputing*, 8(4):305–328, 1995.
47. Alok Choudhary and Rajeev Thakur, "Connected Component Labeling on Coarse Grain Parallel Computers: An Experimental Study," *Journal of Parallel and Distributed Computing*, 20(1):78–83, January 1994.
48. Ravi Ponnusamy, Rajeev Thakur, Alok Choudhary, Kishore Velamakanni, and Geoffrey Fox, "Experimental Performance Evaluation of the CM-5," *Journal of Parallel and Distributed Computing*, 19(3):192–202, November 1993.

## Refereed Proceedings

1. Yong Chen, Chao Chen, Yanlong Yin, Xian-He Sun, Rajeev Thakur, William Gropp, "Rethinking High Performance Computing System Architecture for Scientific Big Data Applications," in *Proceedings of the 14th IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA-16)*, August 2016. (**Best Paper Award**)
2. Hassan Eslami, Anthony Kougkas, Maria Kotsifakou, Theodoros Kasampalis, Kun Feng, Yin Lu, William Gropp, Xian-He Sun, Yong Chen, Rajeev Thakur, "Efficient Disk-to-Disk Sorting: A Case Study in the Decoupled Execution Paradigm," in *Proceedings of the 2015 International Workshop on Data-Intensive Scalable Computing Systems (DISCS)* (held in conjunction with SC15), November 2015.
3. Swann Perarnau, Rajeev Thakur, Kamil Iskra, Ken Raffanetti, Franck Cappello, Rinku Gupta, Pete Beckman, Marc Snir, Henry Hoffmann, Martin Schulz, and Barry Rountree, "Distributed Monitoring and Management of Exascale Systems in the Argo Project," in *Proceedings of the 15th IFIP International Conference on Distributed Applications and Interoperable Systems (DAIS)*, June 2015.

4. Yanlong Yin, Antonios Kougkas, Kun Feng, Hassan Eslami, Yin Lu, Xian-He Sun, Rajeev Thakur, and William Gropp, "Rethinking Key-Value Store for Parallel I/O Optimization," in *Proceedings of the 3rd International Workshop on Data-Intensive Scalable Computing Systems (DISCS)* (held in conjunction with SC14), November 2014.
5. Chao Chen, Yong Chen, Kun Feng, Yanlong Yin, Hassan Eslami, Rajeev Thakur, Xian-He Sun, and William D. Gropp, "Decoupled I/O for Data-Intensive High Performance Computing," in *Proceedings of the 7th International Workshop on Parallel Programming Models and Systems Software for High-End Computing (P2S2)*, September 2014.
6. Xin Zhao, Pavan Balaji, William Gropp, and Rajeev Thakur, "Optimization Strategies for MPI-Interoperable Active Messages," in *Proceedings of the 13th IEEE International Conference on Scalable Computing and Communication (ScalCom 2013)*, December 2013. (**Best Paper Award**)
7. Xin Zhao, Pavan Balaji, William Gropp, and Rajeev Thakur, "MPI-Interoperable Generalized Active Messages," in *Proceedings of the 19th IEEE International Conference on Parallel and Distributed Systems (ICPADS'13)*, December 2013.
8. James Dinan, Pavan Balaji, David Goodell, Douglas Miller, Marc Snir, and Rajeev Thakur, "Enabling MPI Interoperability Through Flexible Communication Endpoints," in *Proceedings of the 20th European MPI Users' Group Meeting (EuroMPI 2013)*, September 2013.
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110. Rajeev Thakur, William Gropp, and Ewing Lusk, "An Experimental Evaluation of the Parallel I/O Systems of the IBM SP and Intel Paragon Using a Production Application," in *Proceedings of the 3rd International Conference of the Austrian Center for Parallel Computation with*

- special emphasis on Parallel Databases and Parallel I/O*, Lecture Notes in Computer Science 1127, Springer-Verlag, September 1996, pp. 24–35.
111. Kevin Roe, Rajeev Thakur, Thong Dang, and Edward Bogucz, “Implementation of a 3D Mixing Layer Code on Parallel Computers,” in *Proceedings of AIAA 6th International Aerospace Planes and Hypersonics Technologies Conference*, April 1995.
  112. Rajeev Thakur, Rajesh Bordawekar, and Alok Choudhary, “Compiler and Runtime Support for Out-of-Core HPF Programs,” in *Proceedings of International Conference on Supercomputing*, July 1994, pp. 382–391.
  113. Rajeev Thakur, Alok Choudhary, and Geoffrey Fox, “Runtime Array Redistribution in HPF Programs,” in *Proceedings of Scalable High Performance Computing Conference*, May 1994, pp. 309–316.
  114. Rajeev Thakur, Rajesh Bordawekar, and Alok Choudhary, “Compilation of Out-of-Core Data Parallel Programs for Distributed Memory Machines,” in *Proceedings of the Workshop on I/O in Parallel Computer Systems at IPPS '94*, April 1994, pp. 54–72.
  115. Rajeev Thakur and Alok Choudhary, “All-to-All Communication on Meshes with Wormhole Routing,” in *Proceedings of 8th International Parallel Processing Symposium*, April 1994, pp. 561–565.
  116. Rajeev Thakur, Alok Choudhary, and Geoffrey Fox, “Complete Exchange on a Wormhole Routed Mesh,” in *Proceedings of MASCOTS '94*, January 1994, pp. 131–135.
  117. Ravi Ponnusamy, Rajeev Thakur, Alok Choudhary, and Geoffrey Fox, “Scheduling Regular and Irregular Communication Patterns on the CM-5,” in *Proceedings of Supercomputing '92*, November 1992, pp. 394–402. **(Best Student Paper Award in the category of performance measurement)**
  118. Alok Choudhary and Rajeev Thakur, “Evaluation of Connected Component Labeling Algorithms on Shared and Distributed Memory Multiprocessors,” in *Proceedings of 6th International Parallel Processing Symposium*, March 1992, pp. 362–365.

### **Other Conference Proceedings**

1. Rajeev Thakur and William Gropp, “Open Issues in MPI Implementation,” in *Proceedings of the 12th Asia-Pacific Computer Systems Architecture Conference (ACSAC 2007)*, August 2007, pp. 327–338.
2. Rajeev Thakur, Alok Choudhary, Rajesh Bordawekar, Sachin More, and K. Sivaram, “PASSION Runtime Library for the Intel Paragon,” in *Proceedings of the Intel Supercomputer User's Group Conference*, June 1995.
3. Rajeev Thakur, Rajesh Bordawekar, Alok Choudhary, Ravi Ponnusamy, and Tarvinder Singh, “PASSION Runtime Library for Parallel I/O,” in *Proceedings of the Scalable Parallel Libraries Conference*, October 1994, pp. 119–128.
4. Zeki Bozkus, Alok Choudhary, Geoffrey Fox, Tomasz Haupt, Sanjay Ranka, Rajeev Thakur, and J.C. Wang, “Scalable Libraries for High Performance Fortran,” in *Proceedings of Scalable Parallel Libraries Conference*, October 1993, pp. 67–75.
5. Ishfaq Ahmad, Rajesh Bordawekar, Zeki Bozkus, Alok Choudhary, Geoffrey Fox, Kanchana Parasuram, Ravi Ponnusamy, Rajeev Thakur, and Sanjay Ranka, “Fortran 90D Intrinsic Functions on Distributed Memory Machines: Implementation and Scalability,” in *Proceedings of 26th Hawaii International Conference on System Sciences*, January 1993.

## Technical Reports

1. Jack Dongarra, Pete Beckman, Terry Moore, Patrick Aerts, Giovanni Aloisio, Jean-Claude Andre, David Barkai, Jean-Yves Berthou, Taisuke Boku, Bertrand Braunschweig, Franck Cappello, Barbara Chapman, Xuebin Chi, Alok Choudhary, Sudip Dosanjh, Thom Dunning, Sandro Fiore, Al Geist, Bill Gropp, Robert Harrison, Mark Hereld, Michael Heroux, Adolffy Hoisie, Koh Hotta, Zhong Jin, Yutaka Ishikawa, Fred Johnson, Sanjay Kale, Richard Kenway, David Keyes, Bill Kramer, Jesus Labarta, Alain Lichnewsky, Thomas Lippert, Bob Lucas, Barney Maccabe, Satoshi Matsuoka, Paul Messina, Peter Michielse, Bernd Mohr, Matthias S. Mueller, Wolfgang E. Nagel, Hiroshi Nakashima, Michael E Papka, Dan Reed, Mitsuhsa Sato, Ed Seidel, John Shalf, David Skinner, Marc Snir, Thomas Sterling, Rick Stevens, Fred Streitz, Bob Sugar, Shinji Sumimoto, William Tang, John Taylor, Rajeev Thakur, Anne Trefethen, Mateo Valero, Aad van der Steen, Jeffrey Vetter, Peg Williams, Robert Wisniewski, and Kathy Yelick, "The International Exascale Software Project Roadmap," *International Journal of High Performance Computing Applications*, 25(1):3–60, February 2011.
2. Tarek El-Ghazawi, Francois Cantonnet, Proshanta Saha, Rajeev Thakur, Rob Ross, and Dan Bonachea, "UPC-IO: A Parallel I/O API for UPC," Version 1.0, Technical Report, High Performance Computing Laboratory, George Washington University, July 2004.
3. Rajeev Thakur, Ewing Lusk, and William Gropp, "Users Guide for ROMIO: A High-Performance, Portable MPI-IO Implementation," Technical Memorandum ANL/MCS-TM-234, Mathematics and Computer Science Division, Argonne National Laboratory, revised September 2000.
4. Rajeev Thakur, Ewing Lusk, and William Gropp, "I/O Characterization of a Portable Astrophysics Application on the IBM SP and Intel Paragon," Preprint MCS-P534-0895, Mathematics and Computer Science Division, Argonne National Laboratory, August 1995.
5. Rajesh Bordawekar, Rajeev Thakur, and Alok Choudhary, "Data Access Reorganizations in Compiling Out-of-Core Data Parallel Programs on Distributed Memory Machines," NPAC Technical Report SCCS-622, Syracuse University, revised September 1994.
6. Alok Choudhary, Rajesh Bordawekar, Michael Harry, Rakesh Krishnaiyer, Ravi Ponnusamy, Tarvinder Singh, and Rajeev Thakur, "PASSION: Parallel and Scalable Software for I/O," NPAC Technical Report SCCS-636, Syracuse University, September 1994.

## Invited Talks

1. "Future of MPI," DOE Programming Environments Workshop, Rockville, Maryland, March 2015.
2. "Future Node Architectures and their Implications for MPI," Workshop on Clusters, Clouds, and Data for Scientific Computing, Dareizé, France, September 2014.
3. "Evolving MPI to Address the Challenges of Exascale Systems," 20th European MPI Users Group Meeting (EuroMPI 2013), Madrid, Spain, September 2013.
4. "Update on MPI and OS/R Activities at Argonne," Ninth Workshop of the INRIA-Illinois Joint Laboratory on Petascale Computing, Lyon, France, June 2013.
5. "Challenges for Communication Libraries and Runtime Systems at Exascale," Workshop on Clusters, Clouds, and Data for Scientific Computing, Dareizé, France, September 2012.
6. "Recent Activities in Programming Models and Runtime Systems at ANL," Seventh Workshop of the INRIA-Illinois Joint Laboratory on Petascale Computing, Rennes, France, June 2012.
7. "Challenges in Scaling MPI to Exascale," First Workshop of the MCS-ISCAS Joint Laboratory for Parallel Processing and Computing Technologies, Beijing, China, May 2012.

8. "Challenges in Scaling MPI to Exascale," Sixth Workshop of the INRIA-Illinois Joint Laboratory on Petascale Computing, Champaign, Illinois, November 2011.
9. "Future Directions in MPI," Workshop on Clusters, Clouds, and Grids for Scientific Computing, Asheville, North Carolina, September 2010.
10. "MPI at Exascale," SciDAC 2010 conference, Chattanooga, Tennessee, July 2010.
11. "Programming Models for Large-Scale Parallel Machines," Workshop on Trends, Technologies and Collaborative Opportunities in High Performance Computing (WTTC09), Mae Fah Luang University, Chiang Rai, Thailand, May 2009.
12. "Programming Models for Large-Scale Parallel Machines," Workshop on Trends, Technologies and Collaborative Opportunities in High Performance and Grid Computing (WTTC2008), Phuket, Thailand, June 2008.
13. "MPI: Enabling Portable, High-Performance Parallel Computing," SHARCNET Workshop, University of Western Ontario, London, Ontario, Canada, October 2003.
14. "MPICH on Clusters: Future Directions," Linux Supercluster Users Conference, Albuquerque, New Mexico, September 2000.
15. "MPI-IO: A Standard, Portable API for High-Performance Parallel I/O," Extreme Linux Workshop, Monterey, California, June 1999.
16. "Portable Parallel I/O on Cluster Computers," Workshop on High Performance Cluster Computing at the 11th ACM International Conference on Supercomputing, Vienna, Austria, July 1997.

## Seminars and Colloquia

1. "Future of MPI and MPICH," Électricité de France, Paris, France, June 2015.
2. "Future Directions in MPI," Fermilab Computing Division Seminar, February 2011.
3. "Programming Extreme Scale Parallel Computers," Department of Electrical Engineering and Computer Science, University of Central Florida, July 2010.
4. "MPI on Millions of Cores," LANS seminar, Argonne National Laboratory, January 2010.
5. "Multithreaded MPI for Multicore Systems," IBM Research Multicore Seminar Series, May 2008.
6. "Programming Models for Next Generation Parallel Machines," IBM T. J. Watson Research Center, Yorktown Heights, NY, March 2008.
7. "MPICH2: Recent Developments and Future Directions," University of Chicago Computation Institute Brown Bag Seminar Series, June 2006.
8. "MPI: Enabling Portable, High-Performance Parallel Programming," Mahidol University, Bangkok, Thailand, May 2006.
9. "The MPI Standard: Design, Implementation, and Verification," University of Utah School of Computing, Salt Lake City, Utah, November 2005.
10. "MPI-IO: Design, Implementation, and Performance," Compaq Computer Corporation, Nashua, New Hampshire, March 2002.
11. "Scalable Systems Software for Large Linux Clusters," IBM T. J. Watson Research Center, Yorktown Heights, New York, September 2000.
12. "Portable Parallel I/O with MPI-IO," Sandia National Laboratories, Albuquerque, New Mexico, January 1998.
13. "Portable Parallel I/O with MPI-IO," Hewlett-Packard, Richardson, Texas, October 1997.
14. "ROMIO: A High-Performance, Portable MPI-IO Implementation," Aurora Project Workshop, European Centre for Parallel Computing at Vienna (VCPC), Austria, July 1997.

15. "A Framework for High-Performance, Portable Parallel I/O," Department of Computer Science, Indiana University-Purdue University at Indianapolis, March 1997.

## **Other Presentations**

1. "Evolving MPI to Address the Challenges of Exascale Systems," DOE X-Stack Portfolio PI Meeting, Berkeley, CA, March 2013
2. "Future Directions in MPI," FLASH HEDP Exascale Codesign Center Meeting, Argonne, IL, February 2011.
3. "Implementing MPI on Windows: Comparison with Common Approaches on Unix," 17th European MPI Users' Group Meeting (EuroMPI 2010), Stuttgart, Germany, September 2010.
4. "MPI on a Million Cores," Scientific Data Management SciDAC All-Hands Meeting, Davis, California, October 2009.
5. "Conflict Detection Algorithm to Minimize Locking For MPI-IO Atomicity," 16th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2009), Helsinki, Finland, September 2009.
6. "MPI on a Million Processors," 16th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2009), Helsinki, Finland, September 2009.
7. "The Future of Deep Computing System Software," IBM Deep Computing Institute External Advisory Board Meeting, San Diego, CA, February 2008.
8. "Test Suite for Evaluating Performance of MPI Implementations That Support MPI\_THREAD\_MULTIPLE," 14th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2007), Paris, France, September 2007.
9. "Open Issues in MPI Implementation," 12th Asia-Pacific Computer Systems Architecture Conference (ACSAC 2007), Seoul, Korea, August 2007.
10. "Automatic Memory Optimizations for Improving MPI Derived Datatype Performance," 13th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2006), Bonn, Germany, September 2006.
11. "Opportunities in Parallel I/O for Scientific Data Management," Scientific Data Management SciDAC All-Hands Meeting, Raleigh, North Carolina, October 2005.
12. "Implementing Byte-Range Locks Using MPI One-Sided Communication," 12th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2005), Sorrento, Italy, September 2005.
13. "An Evaluation of Implementation Options for MPI One-Sided Communication," 12th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2005), Sorrento, Italy, September 2005.
14. "MPI-IO Needs and Requirements," DARPA HPCS I/O Workshop, Washington, DC, July 2005.
15. "I/O Consistency Semantics and Performance Implications," 9th Workshop on Distributed Supercomputing (SOS9), Davos, Switzerland, March 2005.
16. "Minimizing Synchronization Overhead in the Implementation of MPI One-Sided Communication," 11th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2004), Budapest, Hungary, September 2004.
17. "Improving the Performance of MPI Derived Datatypes by Optimizing Memory-Access Cost," IEEE International Conference on Cluster Computing (Cluster 2003), Hong Kong, December 2003.
18. "Improving the Performance of Collective Operations in MPICH," 10th European PVM/MPI Users' Group Meeting (Euro PVM/MPI 2003), Venice, Italy, September 2003.

19. "I/O on Clusters," 7th Workshop on Distributed Supercomputing (SOS7), Durango, Colorado, March 2003.
20. "Parallel I/O Efforts at Argonne: A Brief Introduction," Microsoft Corporation, Redmond, Washington, April 2002.
21. "Chiba City: An Open Source Computer Science Testbed," Cluster Computing Workshop, New Orleans, Louisiana, March 2000.
22. "OS, Message Passing, and Runtime Tools," Panel Session on OS, Message Passing, and Runtime Tools, Cluster Computing Workshop, New Orleans, Louisiana, March 2000.
23. "MPI I/O Perspectives," Panel Session on Scalable I/O, Cluster Computing Workshop, New Orleans, Louisiana, March 2000.
24. "How Can File Systems Be Designed to Better Support MPI-IO?" Scalable Global Parallel File System Meeting, Santa Fe, New Mexico, September 1999.
25. "On Implementing MPI-IO Portably and with High Performance," Sixth Workshop on I/O in Parallel and Distributed Systems, Atlanta, Georgia, May 1999.
26. "High-Performance Parallel I/O in MPI," MPICH-LANL meeting, Los Alamos, New Mexico, March 1999.
27. "Data Sieving and Collective I/O in ROMIO," 7th Symposium on the Frontiers of Massively Parallel Computation, Annapolis, Maryland, February 1999.
28. "Achieving High-Performance with MPI-IO," SIO-ASCI Meeting, Livermore, California, January 1999.
29. "A Case for Using MPI's Derived Datatypes to Improve I/O Performance," SC98: High Performance Networking and Computing, Orlando, Florida, November 1998.
30. "ROMIO: A High-Performance, Portable MPI-IO Implementation," Scalable I/O Initiative Sponsors Review meeting, Rice University, Houston, Texas, September 1997.
31. "ROMIO: A High-Performance, Portable MPI-IO Implementation," Scalable I/O Initiative All-Hands Meeting, Center for Advanced Computing Research, California Institute of Technology, June 1997.
32. "MPI-IO and Its Implementation," 2nd Midwest Workshop on High-Performance Systems, University of Illinois at Chicago, May 1997.
33. "Portable Implementation of Parallel-I/O APIs," Minisymposium on Parallel I/O at the Eighth SIAM Conference on Parallel Processing for Scientific Computing, Minneapolis, Minnesota, March 1997.
34. "Implementing High-Level Interfaces on the SIO Low-Level API," Scalable I/O Roundtable at Supercomputing '96, Pittsburgh, Pennsylvania, November 1996.
35. "An Abstract-Device Interface for Implementing Portable Parallel-I/O Interfaces," 6th Symposium on the Frontiers of Massively Parallel Computation, Annapolis, Maryland, October 1996.
36. "An Experimental Evaluation of the Parallel I/O Systems of the IBM SP and Intel Paragon Using a Production Application," 3rd International Conference of the Austrian Center for Parallel Computation with special emphasis on Parallel Databases and Parallel I/O, Klagenfurt, Austria, September 1996.
37. "Portable Implementation of MPI-IO," Panel discussion on MPI-IO at the MPI Developers Conference and Users Group Meeting, Notre Dame, Indiana, July 1996.
38. "An Abstract-Device Interface for Parallel I/O," Annual Meeting of the Center for Research on Parallel Computation, Argonne, Illinois, May 1996.
39. "I/O Characterization of a Portable Astrophysics Application," Scalable I/O Workshop at Supercomputing '95, San Diego, California, December 1995.

40. "PASSION Runtime Library for Parallel I/O," Scalable Parallel Libraries Conference, Mississippi State University, Starkville, Mississippi, October 1994.
41. "Compiler and Runtime Support for Out-of-Core HPF Programs," International Conference on Supercomputing, Manchester, UK, July 1994.
42. "Runtime Array Redistribution in HPF Programs," Scalable High Performance Computing Conference, Knoxville, Tennessee, May 1994.
43. "Scheduling Regular and Irregular Communication Patterns on the CM-5," Supercomputing '92, Minneapolis, Minnesota, November 1992.

## Tutorials

1. "Advanced MPI Programming," at SC15: International Conference on High Performance Computing, Networking, Storage, and Analysis, Austin, Texas, November 2015. (with Pavan Balaji, William Gropp, and Torsten Hoefler)
2. "Advanced MPI Programming," at SC14: International Conference on High Performance Computing, Networking, Storage, and Analysis, New Orleans, Louisiana, November 2014. (with Pavan Balaji, William Gropp, and Torsten Hoefler)
3. "Advanced MPI Programming," at SC13: International Conference on High Performance Computing, Networking, Storage, and Analysis, Denver, Colorado, November 2013. (with Pavan Balaji, James Dinan, and Torsten Hoefler)
4. "Advanced MPI," at SC12: International Conference on High Performance Computing, Networking, Storage, and Analysis, Salt Lake City, Utah, November 2012. (with William Gropp, Ewing Lusk, and Robert Ross)
5. "Advanced MPI," at the 19th European MPI Users' Group Meeting (EuroMPI 2012), Vienna, Austria, September 2012. (with William Gropp and Ewing Lusk)
6. "Advanced MPI," at SC11: International Conference on High Performance Computing, Networking, Storage, and Analysis, Seattle, Washington, November 2011. (with William Gropp, Ewing Lusk, and Robert Ross)
7. "MPI and Parallel I/O," CHPC 2011 Winter School at The University of the Witwatersrand, Johannesburg, South Africa, July 2011.
8. "Advanced MPI," at SC10: International Conference on High Performance Computing, Networking, Storage, and Analysis, New Orleans, Louisiana, November 2010. (with William Gropp, Ewing Lusk, and Robert Ross)
9. "MPI and Parallel I/O," PRACE Autumn School on High Performance Computing, Barcelona Supercomputing Center, Barcelona, Spain, October 2010.
10. "Programming in MPI for Performance and MPI at Exascale," CScADS Summer Workshop on Leadership-class Machines, Petascale Applications, and Performance Strategies, Snowbird, Utah, July 2010
11. "Advanced MPI," at SC09: International Conference on High Performance Computing, Networking, Storage, and Analysis, Portland, Oregon, November 2009. (with William Gropp, Ewing Lusk, and Robert Ross)
12. "Advanced MPI," at the Workshop on Trends, Technologies and Collaborative Opportunities in High Performance Computing (WTTTC09), Bangkok, Thailand, May 2009.
13. "Advanced MPI," at SC08: International Conference on High Performance Computing, Networking, Storage, and Analysis, Austin, Texas, November 2008. (with William Gropp, Ewing Lusk, and Robert Ross)



14. "Advanced MPI," at the Workshop on Trends, Technologies and Collaborative Opportunities in High Performance and Grid Computing (WTTTC2008), Bangkok, Thailand, June 2008.
15. "Parallel I/O in Practice," at SC07: International Conference on High Performance Computing, Networking, Storage, and Analysis, Reno, Nevada, November 2007. (with Robert Ross, Robert Latham, and Bill Loewe)
16. "Advanced MPI," at SC07: International Conference on High Performance Computing, Networking, Storage, and Analysis, Reno, Nevada, November 2007. (with William Gropp, Ewing Lusk, and Robert Ross)
17. "Parallel I/O in Practice," at SC06: International Conference on High Performance Computing, Networking, Storage, and Analysis, Tampa, Florida, November 2006. (with Robert Ross, Robert Latham, and Bill Loewe)
18. "Advanced MPI: I/O and One-Sided Communication," at SC06: International Conference on High Performance Computing, Networking, Storage, and Analysis, Tampa, Florida, November 2006. (with William Gropp, Ewing Lusk, and Robert Ross)
19. "Introduction to MPI-IO and MPI-2," 12th Annual SDSC Summer Institute, San Diego Supercomputer Center, San Diego, California, July 2006.
20. "MPI on the Grid," at the 6th IEEE International Symposium on Cluster Computing and the Grid (CCGrid 2006), Singapore, May 2006. (with William Gropp)
21. "Parallel I/O in Practice," at SC|05: International Conference on High Performance Computing, Networking, and Storage, Seattle, Washington, November 2005. (with Robert Ross, Robert Latham, and Bill Loewe)
22. "Advanced MPI: I/O and One-Sided Communication," at SC|05: International Conference on High Performance Computing, Networking, and Storage, Seattle, Washington, November 2005. (with William Gropp, Ewing Lusk, and Robert Ross)
23. "Introduction to Parallel I/O and MPI-IO," 11th Annual Computing Institute, San Diego Supercomputer Center, San Diego, California, July 2005.
24. "Advanced MPI: I/O and One-Sided Communication," at SC2004: High Performance Computing, Networking, and Storage Conference, Pittsburgh, Pennsylvania, November 2004. (with William Gropp, Ewing Lusk, and Robert Ross)
25. "Using MPI-2: Advanced Features of the Message-Passing Interface," at the 4th IEEE/ACM International Symposium on Cluster Computing and the Grid (CCGrid 2004), Chicago, Illinois, April 2004. (with Robert Ross)
26. "Using MPI-2: Advanced Features of the Message-Passing Interface," at the Second International Workshop on Grid and Cooperative Computing (GCC 2003), Shanghai, China, December 2003. (with Robert Ross)
27. "Using MPI-2: A Tutorial on Advanced Features of the Message-Passing Interface Standard," at SC2003: High Performance Networking and Computing, Phoenix, Arizona, November 2003. (with William Gropp, Ewing Lusk, and Robert Ross)
28. "Using MPI for Peak Performance," SHARCNET Workshop, Univ. of Western Ontario, London, Ontario, Canada, October 2003. (with Brian Toonen)
29. "Using MPI-2: A Tutorial on Advanced Features of the Message-Passing Interface Standard," at SC2002: High Performance Networking and Computing, Baltimore, Maryland, November 2002. (with William Gropp, Ewing Lusk, and Robert Ross)
30. "Parallel I/O in MPI-2," in tutorial on Porting to PACI Architectures, NPACI All-Hands Meeting, San Diego, California, March 2002.

31. "Using MPI-2: A Tutorial on Advanced Features of the Message-Passing Interface," at SC2001: High Performance Networking and Computing, Denver, Colorado, November 2001. (with William Gropp, Ewing Lusk, and Robert Ross)
32. "Using MPI-2: A Tutorial on Advanced Features of the Message-Passing Interface," at SC2000: High Performance Networking and Computing, Dallas, Texas, November 2000. (with William Gropp and Ewing Lusk)
33. "Tuning MPI Applications for Peak Performance," at SC99: High Performance Networking and Computing, Portland, Oregon, November 1999. (with William Gropp and Ewing Lusk)
34. "Tuning MPI Applications for Peak Performance," at SC98: High Performance Networking and Computing, Orlando, Florida, November 1998. (with William Gropp and Ewing Lusk)
35. "Introduction to Performance Issues in Using MPI for Communication and I/O," at the 7th IEEE International Conference on High Performance Distributed Computing, Chicago, Illinois, July 1998. (with William Gropp)